

CPU

block0 v	block0 t=2	block0 t=3
block1 v	block1 t=0	block1 t=1
block2 v	block2 t=0	block2 t=1
block3 v	block3 t=0	block3 t=1
block4 v	block4 t=0	block4 t=1
block5 v	block5 t=0	block5 t=1
block6 v	block6 t=0	block6 t=1

GPU

block0 v	block0 t=0	block0 t=1	block0 t=2	block0 t=3
block1 v	block1 t=0	block1 t=1	block1 t=2	block1 t=3
block2 v	block2 t=0	block2 t=1	block2 t=2	block2 t=3
block3 v	block3 t=0	block3 t=1	block3 t=2	
block4 v	block4 t=0	block4 t=1		
block5 v	block5 t=0	block5 t=1		



transfer in



update



transfer out