

CPU

block0 v	block0 t=4	block0 t=5
block1 v	block1 t=0	block1 t=1
block2 v	block2 t=0	block2 t=1
block3 v	block3 t=0	block3 t=1
block4 v	block4 t=0	block4 t=1
block5 v	block5 t=0	block5 t=1
block6 v	block6 t=0	block6 t=1
block7 v	block7 t=0	block7 t=1
block8 v	block8 t=0	block8 t=1
block9 v	block9 t=0	block9 t=1
block10 v	block10 t=0	block10 t=1
block11 v	block11 t=0	block11 t=1

GPU1

block0 v	block0 t=2	block0 t=3	block0 t=4	block0 t=5
block1 v	block1 t=2	block1 t=3	block1 t=4	block1 t=5
block2 v	block2 t=2	block2 t=3	block2 t=4	block2 t=5
block3 v	block3 t=2	block3 t=3	block3 t=4	
block4 v	block4 t=2	block4 t=3		
block5 v	block5 t=2	block5 t=3		

GPU0

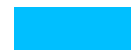
block5 v	block5 t=0	block5 t=1	block5 t=2	block5 t=3
block6 v	block6 t=0	block6 t=1	block6 t=2	block6 t=3
block7 v	block7 t=0	block7 t=1	block7 t=2	block7 t=3
block8 v	block8 t=0	block8 t=1	block8 t=2	
block9 v	block9 t=0	block9 t=1		
block10 v	block10 t=0	block10 t=1		



transfer in



update



transfer out